

Preliminary Data Sheet Supplement

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| Subject: | Changes in Specification Limitations |
| Data Sheet Concerned: | DAC 3560C 6251-588-2PD, Edition Sept. 8, 2004 |
| Supplement: | No. 3/ 6251-588-2PDS |
| Edition: | Dec. 9, 2004 |

Changes in the limitations of the specifications for the DAC 3560C:

The limit values for the input resistance (RI) for the analog audio inputs have been changed.

1. Characteristics (LDO Mode)

Unless noted otherwise: LSVDD = VBAT = 3.6 V, EPVDD = HPVDD = CPIN = PVDD = VLDO = 2.85 V (LDO mode), T_A = 0 °C ... 85 °C. Typical values are at T_A = 25 °C..

| Symbol | Parameter | Pin Name | Limit Values | | | Unit | Test Conditions |
|--|------------------|----------|--------------|------|------|------|-----------------|
| | | | Min. | Typ. | Max. | | |
| ANALOG AUDIO INPUTS - AIN, AUXL, AUXR | | | | | | | |
| RI | Input Resistance | | 9 | 13 | 18 | kΩ | Gain = +20 dB |
| | | | 50 | 75 | 100 | | Gain = 0 dB |
| | | | 90 | 130 | 170 | | Gain = -20 dB |